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SIGNATURE

REGISTRATION NO. 43,605

Date

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872039586 US		
ncurrently Herewith		
Paul A. LaBerge		
Yet Assigned		
Not Yet Assigned		
501128.01		
EE CALCULATION (Continued)		
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ee 6) Fee Description Fe	ee paid	
65 Surcharge - Late filing fee or oath	\$	
Surcharge - late provisional filing fee or cover sheet	\$	
130 Non-English specification	\$	
For Filing a Request for Reexamination	\$	
300 Publication (early or Republication)	\$	
55 Extension for reply within first month	\$	
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160 Filing a brief in support of an appeal	\$	
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180 Submission of IDS Recording each patent assignment	\$	
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Subtotal (3)	\$ 40	
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Submitted by:

Name: Kimton N. Eng Reg. No.: 43,605 Telephone: (206) 903-8800

Signature:

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Date: Felowary 21,2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Paul A. LaBerge

Attorney Docket No.: 501128.01

Filed

: February 21, 2002

Title

: MEMORY BUS POLARITY INDICATOR SYSTEM AND METHOD FOR

REDUCING THE AFFECTS OF SIMULTANEOUS SWITCHING OUTPUTS

(SSO) ON MEMORY BUS TIMING

CERTIFICATE OF MAILING BY "EXPRESS MAIL"

BOX PATENT APPLICATION Commissioner for Patents Washington, DC 20231

Sir:

I hereby certify that the enclosures listed below are being deposited with the United States Postal Service "EXPRESS MAIL Post Office to Addressee" service under 37 C.F.R. § 1.10, Mailing Label Certificate No. EL 872039586 US, on February 21, 2002, addressed to Box Patent Application, Commissioner for Patents, Washington, DC 20231.

Respectfully submitted,

DORSEY & WHITNEY LLP

/ for West Courier

KNE:asw

Enclosures:

Postcard

Check

PTO/SB/05 Utility Patent Application Transmittal

Application Data Sheet

Fee Transmittal Sheet (+ copy)

General Authorization

Specification, Claims, Abstract (32 pages)

10 Sheets of Drawings (Figures 1-10)

Declaration

Election and Power of Attorney

Form PTO-1595

Assignment



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Paul A. LaBerge

Attorney Docket No.: 501128.01

Filed

: Concurrently Herewith

Title

: MEMORY BUS POLARITY INDICATOR SYSTEM AND METHOD FOR

REDUCING THE AFFECTS OF SIMULTANEOUS SWITCHING OUTPUTS (SSO)

ON MEMORY BUS TIMING

GENERAL AUTHORIZATION

Box Patent Application Commissioner of Patents Washington, D.C. 20231

Sir:

With respect to the above-identified application, the Commissioner is authorized to charge any fees under 37 C.F.R. §§ 1.16 and 1.17 which may be required and to treat any concurrent or future reply requiring a petition for an extension of time under 37 C.F.R. § 1.136(a)(3) for its timely submission as incorporating a petition therefor for the appropriate length of time and to charge any fees which may be required during the course of prosecution, or credit any overpayment, to Deposit Account No. 50-1266.

Kimton N. Eng

Registration No. 43,605

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